

# Mukul Lokhande

#### Student, Electronics and Telecommunication Engineer

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## Portfolio

### LinkedIn: [**https://www.linkedin.com/in/mukullokhande99/**](https://www.linkedin.com/in/mukullokhande99/)

## Education

### SGGS IET, Nanded

#### B.Tech. ECE, CGPA 6.76

## Professional Experiences

### Intern:

#### RTL Design using Verilog

##### July 2019 - Nov 2019

* Allyone Advanced Stability solutions,Basic Electronics
* Advanced Digital Design Techniques, FPGA work

### TECHNICAL SKILLS:

#### Web Developement using HTML, CSS

##### Hardware Implementation

* FPGA using Verilog
* Cadence Simulation

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